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NXP Semiconductors
IEEE 802.15.4 and
Bluetooth Low Energy
5.0 wireless MCU
K32W061/K32W041
PIO1 4 IO GPIO1 [1]

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Description Nxp
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GPIO1 — General
Purpose digital
Input/Output 1

1. General description - NXP Semiconductors

1. General description
NXP Semiconductors
designed the LPC2468
microcontroller around
a 16-bit/32-bit
ARM7TDMI-S CPU core
with real-time debug
interfaces that include
both JTAG and
embedded trace. The

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Description Nxp
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LPC2468 has 512 kB of on-chip high-speed flash memory. This flash memory includes a special 128-bit wide memory interface and accelerator

1. General description - NXP Semiconductors

General description.
NXP Semiconductors developed the MIFARE Ultralight Nano MF0UN(H)00 for use in a contactless smart

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ticket, smart card or token in combination with a Proximity Coupling Device (PCD). The MF0UN(H)00 is designed to work in an ISO/IEC 14443 Type A compliant environment (see Ref. 1). The target applications include single trip or limited use tickets in public transportation networks, loyalty cards or day passes for events.

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1. General description - NXP Semiconductors

NXP Semiconductors
74LVC1G80 Single D-
type flip-flop; positive-
edge trigger 3.

Ordering information
Table 1. Ordering
information 4. Marking
Table 2. Marking codes
[1] The pin 1 indicator
is located on the lower
left corner of the
device, below the
marking code. 5.
Functional diagram

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Type number Package
Temperature range
Name Description
Version

1. General description

1. General description
... The TJA1042 belongs to the third generation of high-speed CAN transceivers from NXP Semiconductors, offering significant improvements over first- and second-generation ... 2.1

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General ISO
11898-2:2016 and SAE
J2284-1 to SAE J2284-5
compliant

1. General description

NXP Semiconductors
PCA9600 Dual
bidirectional bus buffer
6. Pinning information
6.1 Pinning 6.2 Pin
description Table 3. Pin
description 7.
Functional description
Refer to Figure 1
“Block diagram of

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PCA9600". The
PCA9600 has two
identical buffers
allowing buffering of
SDA and SCL I 2C-bus
signals.

1. General description

NXP Semiconductors
TJF1052i Galvanically
isolated high-speed
CAN transceiver 7.
Functional description
7.1 Operation 7.1.1
Normal mode During
normal operation, the

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TJF1052i transceiver transmits and receives data via bus lines CANH and CANL (see Figure 1 for the block diagram). The differential receiver

1. General description - NXP Semiconductors

NXP Semiconductors
PCA9685 16-channel,
12-bit PWM Fm+ I 2 C-
bus LED controller The
active LOW Output
Enable input pin (OE)

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allows asynchronous control of the LED outputs and can be used to set all the outputs to a defined I 2 C-bus programmable logic

1. General description

General description
NXP Semiconductors designed the LPC2478 microcontroller, powered by the ARM7TDMI-S core, to be a highly integrated

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microcontroller for a wide range of applications that require advanced communications and high quality graphic displays. The LPC2478 microcontroller has 512 kB of on-chip high-speed flash memory.

1. General description - NXP Semiconductors

General description
NXP Semiconductors
has developed

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NTAG203 - NFC Forum
Type 2 Tag compliant
IC - to be used with
NFC enabled devices
according to NFC
Forum technical
specifications (see Ref.
10 and Ref. 11),
according to NFC
Forum
recommendations or
Proximity Coupling
Devices (PCD),
according to ISO/IEC
14443A (see Ref. 1).

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description -

Adafruit Industries

NXP Semiconductors
TEA1733P GreenChip
SMPS control IC

Adjustable soft start
operation Two
protection inputs (e.g.
for input UVP and OVP,
OTP and output OVP)
IC overtemperature
protection 3.

Applications All
applications requiring
efficient and cost-
effective power supply
solutions up to 75 W. 4.

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Ordering information
Table 1.

1. General description - Datasheet catalog
NXP Semiconductors
PCF8583 Clock and calendar with 240 x 8-bit RAM 6.2 Pin description Table 3. Pin description [1] The die paddle (exposed pad) is connected to VSS and should be electrically isolated.

Symbol Pin Type

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Description DIP8
(PCF8583P) SO8
(PCF8583T) HVQFN20
(PCF8583BS) OSCI 1 1
2 input oscillator input,
50 Hz or event-pulse
input

1. General description -

Datasheet catalog

NXP Semiconductors
74LVC241A Octal
buffer/line driver with 5
V tolerant
inputs/outputs; 3-state
[1] The minimum input

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voltage ratings may be exceeded if the input current ratings are observed.

1. General description - Datasheet catalog

1. General description. The TEA152x family STARplug is a Switched Mode Power Supply (SMPS) controller IC that operates directly from the rectified universal mains. It is implemented in the

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high-voltage EZ-HV SOI process, combined with a low-voltage Bipolar Complementary Metal-Oxide Semiconductor (BiCMOS) process.

1. General description - Datasheet catalog

1.1 General description
NPN general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

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2PD601ASL Semiconductors

Datasheet(PDF) 1

Page - NXP

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High-speed switching diodes, encapsulated in small Surface-Mounted Device (SMD)

BAV70T,115 NXP

Semiconductors,

BAV70T,115

Datasheet

Summary of Contents
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1 General

TDA8948J Page 1:
General Description
Product data sheet 1.
General description
The TDA8948J contains
four identical audio
power amplifiers.

NXP SEMICONDUCTORS TDA8948J PRODUCT DATA SHEET Pdf Download.

1.Product profile1.1
General descriptionGen
eral-purpose Zener
diodes in a SOD27

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1 General

(SC-40) small
hermetically sealed
glasspackage.1.2
Features! Total power
dissipation: $P_{tot} \leq 500$
mW! Low differential
resistance! Low
leakage current! AEC-
Q101 qualified
datasheet search,
datasheets, Datasheet
search site for
Electronic Components
and Semiconductors,
integrated circuits,
diodes and other ...

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1 General

NZX2V4A

**datasheet(1/12
Pages) NXP | Single
Zener diodes**

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TDA8944J Page 1:

General Description 1.

General description

The TDA8944J is a dual-channel audio power amplifier with an output power of 2×7 W at an 8Ω load and a 12 V supply.

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Copyright code: d41d8
cd98f00b204e9800998
ecf8427e.